THE CLK16M_H OSCILLATOR IS AN ABRACON ACHL-16.000MHZ-EK
IN THEORY, THE CLK1M8_H OSCILLATOR COULD ALSO BE FROM THE ABRACON ACHL SERIES, BUT I COULD NOT FIND ONE OF THESE STOCKED ANYWHERE, SO I AM USING A (PIN-COMPATIBLE) VISHAY/DALE X032CTELNA1M8432 (XO-523) OSCILLATOR; ANOTHER POSSIBILITY WOULD HAVE BEEN TO USE AN ABRACON ACHL-3.6864MHZ-EK AND CHANGE THE LOGIC

R0203/R0204 ARE A PULL-UP TO 2.5V (PROG IS ON VDDAUX)
R0205/R0206 ARE A PULL-UP TO 2.5V (DONE IS ON VDDAUX)

DGC, 30-OCT-2010
U0301 needs fast read if CCLK GTR 50MHZ
R0305 and R0306 are terminators for the SPI clock
R0313 ensures MISO is a good level even when U0301 is not driving
the KX ROM interface samples MISO while sending the address

PDP10X SYSTEM BOARD
CONFIGURATION/BOOT ROM AND STRAPS
DGC, 31-JUL-2010
I2C USES 1.8K PULLUP
PDP10X SYSTEM BOARD
RTC DEVICE
DGC, 30-OCT-2010
THE EXAR SP3232ECPL IS A PIN-COMPATIBLE SUBSTITUTE FOR THE (SOMewhat MORE EXPENSIVE) MAX3232ECPE

THE TTY DEVICE PORT IS DATA-LEADS-ONLY

THE AUX DEVICE HAS RTS/CTS FOR FLOW CONTROL, DCD SO THE OPERATING SYSTEM CAN TELL IF A DIAL-UP MODEM DISCONNECTS, AND DTR TO ALLOW THE MODEM TO ACTUALLY CONNECT

PDP10X SYSTEM BOARD
TTY DEVICE
DGC, 30- OCT-2010
PIN 39 (DASP) IS NC (PULL-UP IN DISK)
PIN 34 (PDIA) IS NC (PULL-UP IN DISK)
PIN 28 (CSEL) IS GND (MASTER)
PIN 20 (KEY) IS VCC FOR EDC4000

THIS CONNECTOR IS INTENDED TO BE USED WITH
INNODISK EDC4000 40-PIN VERTICAL (DE0H-25G031CXS) MODULES

PDP10X SYSTEM BOARD
DSK DEVICE
DGC, 30-OCT-2010
<table>
<thead>
<tr>
<th>Component Description</th>
<th>Value 1</th>
<th>Value 2</th>
<th>Value 3</th>
<th>Value 4</th>
</tr>
</thead>
<tbody>
<tr>
<td>XC3S500E Datasheet</td>
<td></td>
<td></td>
<td>1.2</td>
<td>2.5</td>
</tr>
<tr>
<td>XC3S500E SYSTEM</td>
<td></td>
<td></td>
<td>3.3</td>
<td></td>
</tr>
<tr>
<td>9312 LUT/FF (100% UTIL), 33 MHz, 50% SW, FAN=5</td>
<td>138</td>
<td></td>
<td>138</td>
<td></td>
</tr>
<tr>
<td>20 BRAM, 100% EN, 33 MHz, 50% SW, 2 PORTS</td>
<td>45</td>
<td></td>
<td>45</td>
<td></td>
</tr>
<tr>
<td>20 MULT, 33 MHz, 50% SW</td>
<td>42</td>
<td></td>
<td>42</td>
<td></td>
</tr>
<tr>
<td>9312 CLOCK LOADS, 33 MHz</td>
<td>25</td>
<td></td>
<td>25</td>
<td></td>
</tr>
<tr>
<td>4 DCMS</td>
<td>4</td>
<td>64</td>
<td>64</td>
<td></td>
</tr>
<tr>
<td>126+32 8 MA LVTTL SLOW, 33 MHz, 50% SW, 50 PF</td>
<td>3</td>
<td>4</td>
<td>803</td>
<td></td>
</tr>
<tr>
<td>DYNAMIC TOTAL (MW)</td>
<td>257</td>
<td>68</td>
<td>803</td>
<td></td>
</tr>
<tr>
<td>DYNAMIC TOTAL (MA)</td>
<td>214</td>
<td>27</td>
<td>243</td>
<td></td>
</tr>
<tr>
<td>STATIC (MA)</td>
<td>106</td>
<td>31</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>TOTAL (MA)</td>
<td>320</td>
<td>58</td>
<td>244</td>
<td>TOTAL (MA)</td>
</tr>
</tbody>
</table>

**Notes:**
- XC3S500E, LDO FROM 3.3, 400+100+300
- 5 X ASG4088-55
- 2 X MAX3232
- 2 X 3.3V OSC (ABRACOM, 30-70 MHZ)
- ENC4000 (DSK, OPERATING)
- WIZ830MJ (OPERATING)

**Additional Components:**
- POWER CONNECTOR, BULK BYPASS
- DGC, 30-OCT-2010