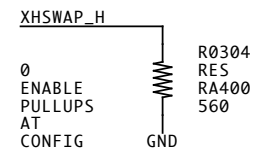
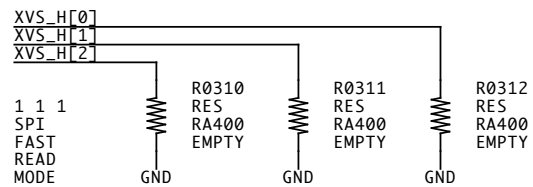
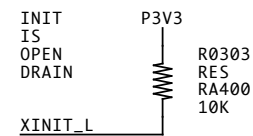
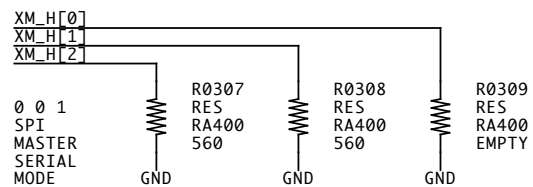
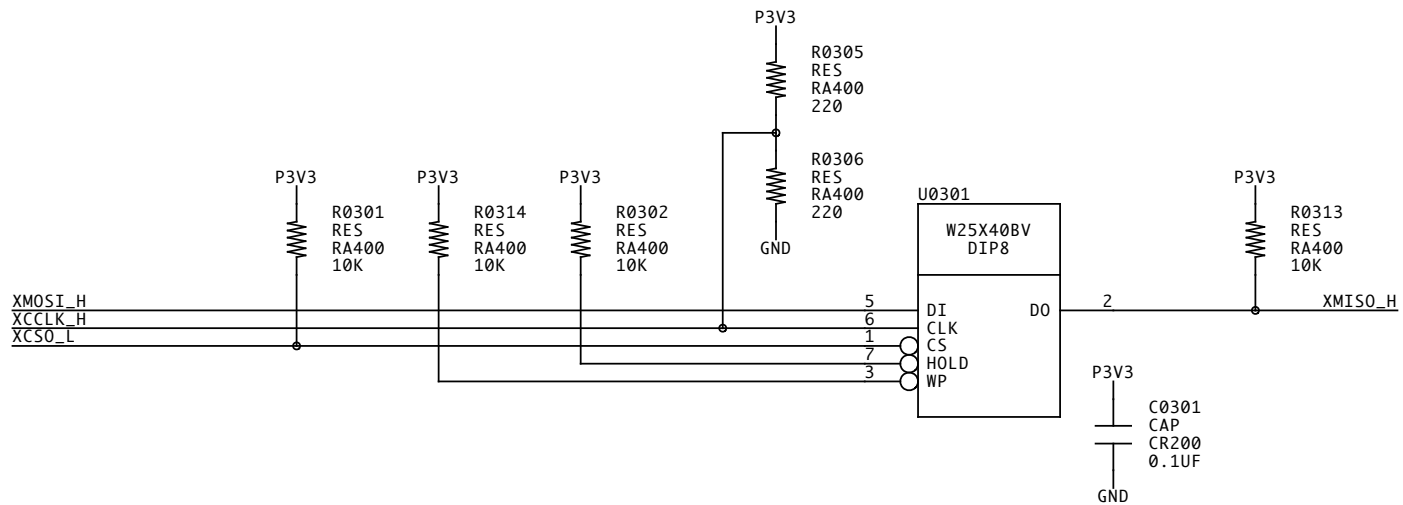


THE CLK16M_H OSCILLATOR IS AN ABRACON ACHL-16.000MHZ-EK
 IN THEORY, THE CLK1M8_H OSCILLATOR COULD ALSO BE FROM THE ABRACON ACHL
 SERIES, BUT I COULD NOT FIND ONE OF THESE STOCKED ANYWHERE,
 SO I AM USING A (PIN-COMPATIBLE) VISHAY/DALE X032CTELNA1M8432 (XO-523)
 OSCILLATOR; ANOTHER POSSIBILITY WOULD HAVE BEEN TO USE AN ABRACON
 ACHL-3.6864MHZ-EK AND CHANGE THE LOGIC

R0203/R0204 ARE A PULL-UP TO 2.5V (PROG IS ON VDDAUX)

R0205/R0206 ARE A PULL-UP TO 2.5V (DONE IS ON VDDAUX)

PDP10X SYSTEM BOARD
 CLOCKS, RESETS, STATUS LED
 DGC, 30-OCT-2010

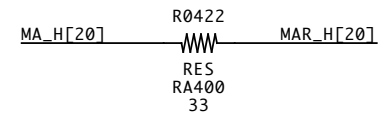
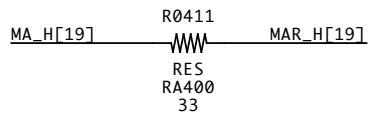
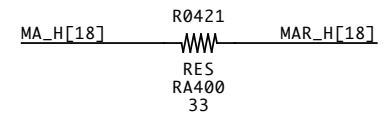
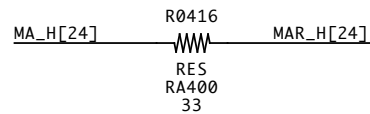
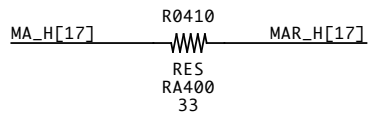
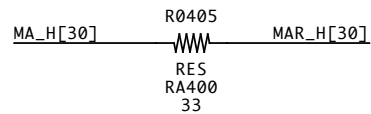
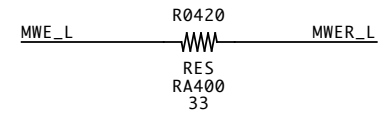
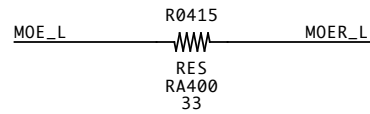
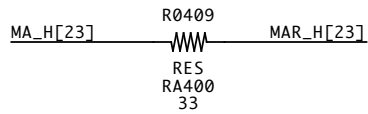
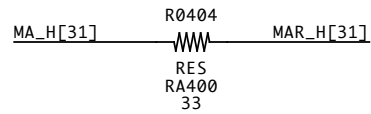
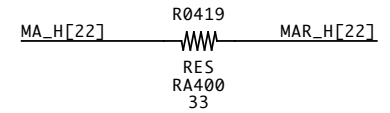
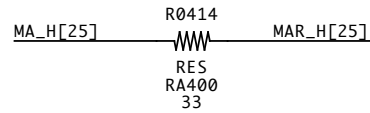
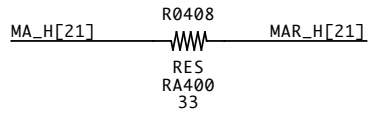
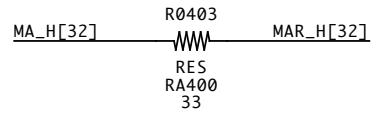
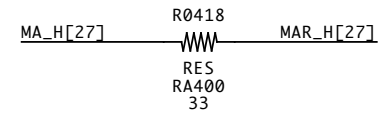
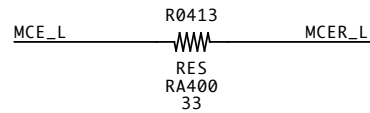
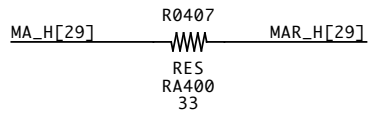
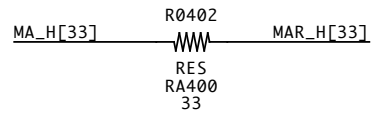
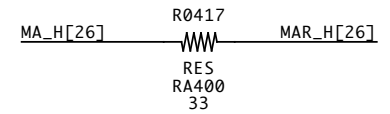
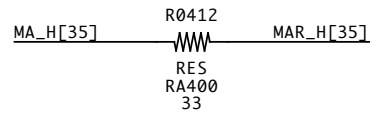
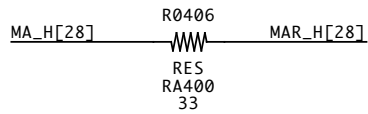
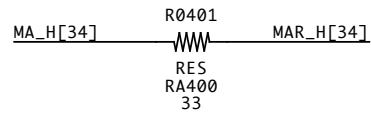


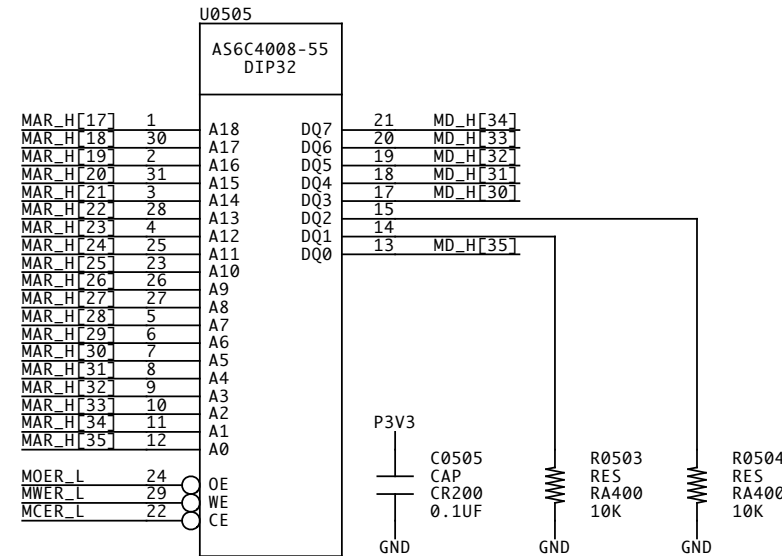
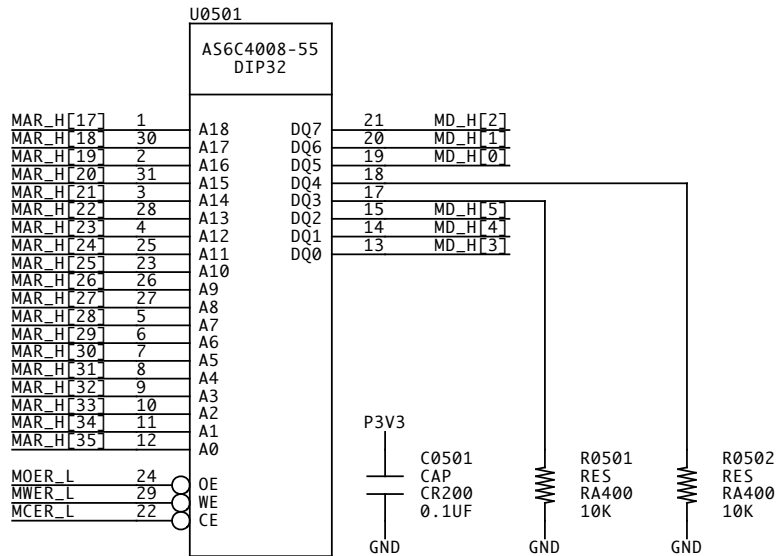
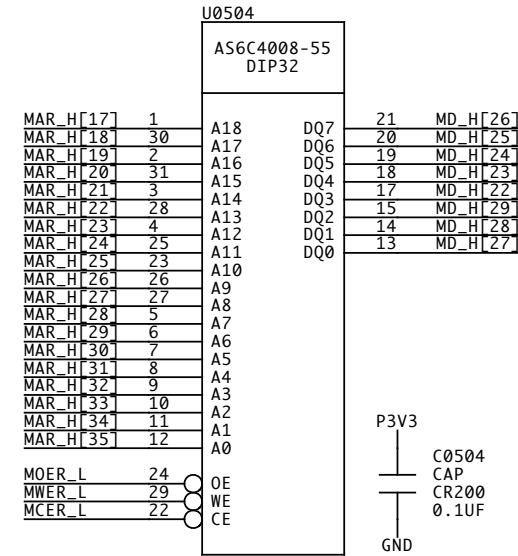
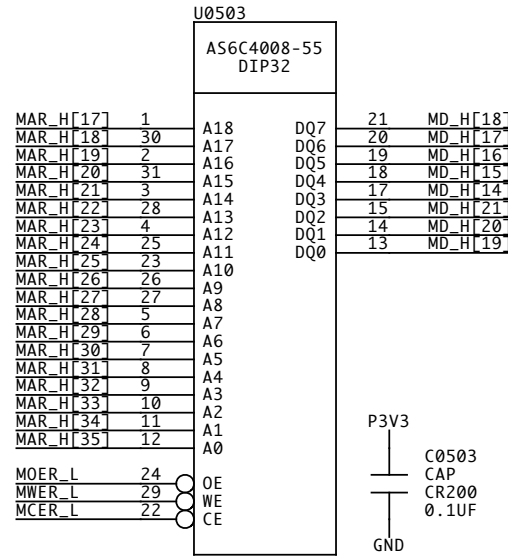
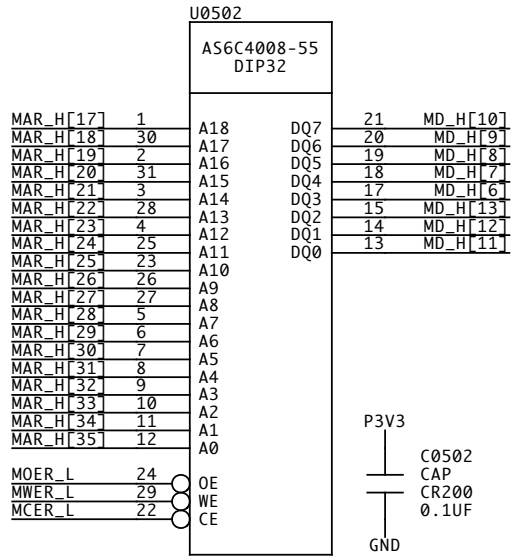
U0301 NEEDS FAST READ IF CCLK GTR 50MHZ

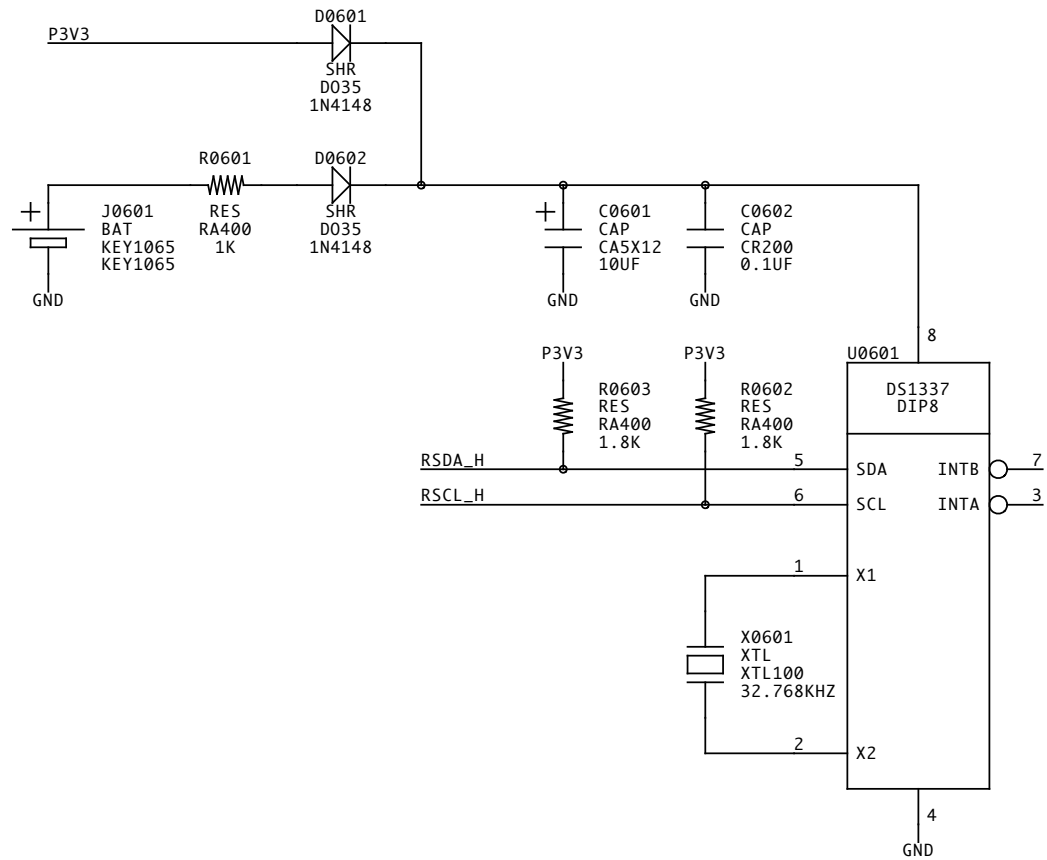
R0305 AND R0306 ARE TERMINATORS FOR THE SPI CLOCK

R0313 ENSURES MISO IS A GOOD LEVEL EVEN WHEN U0301 IS NOT DRIVING THE KX ROM INTERFACE SAMPLES MISO WHILE SENDING THE ADDRESS

PDP10X SYSTEM BOARD
 CONFIGURATION/BOOT ROM AND STRAPS
 DGC, 31-JUL-2010

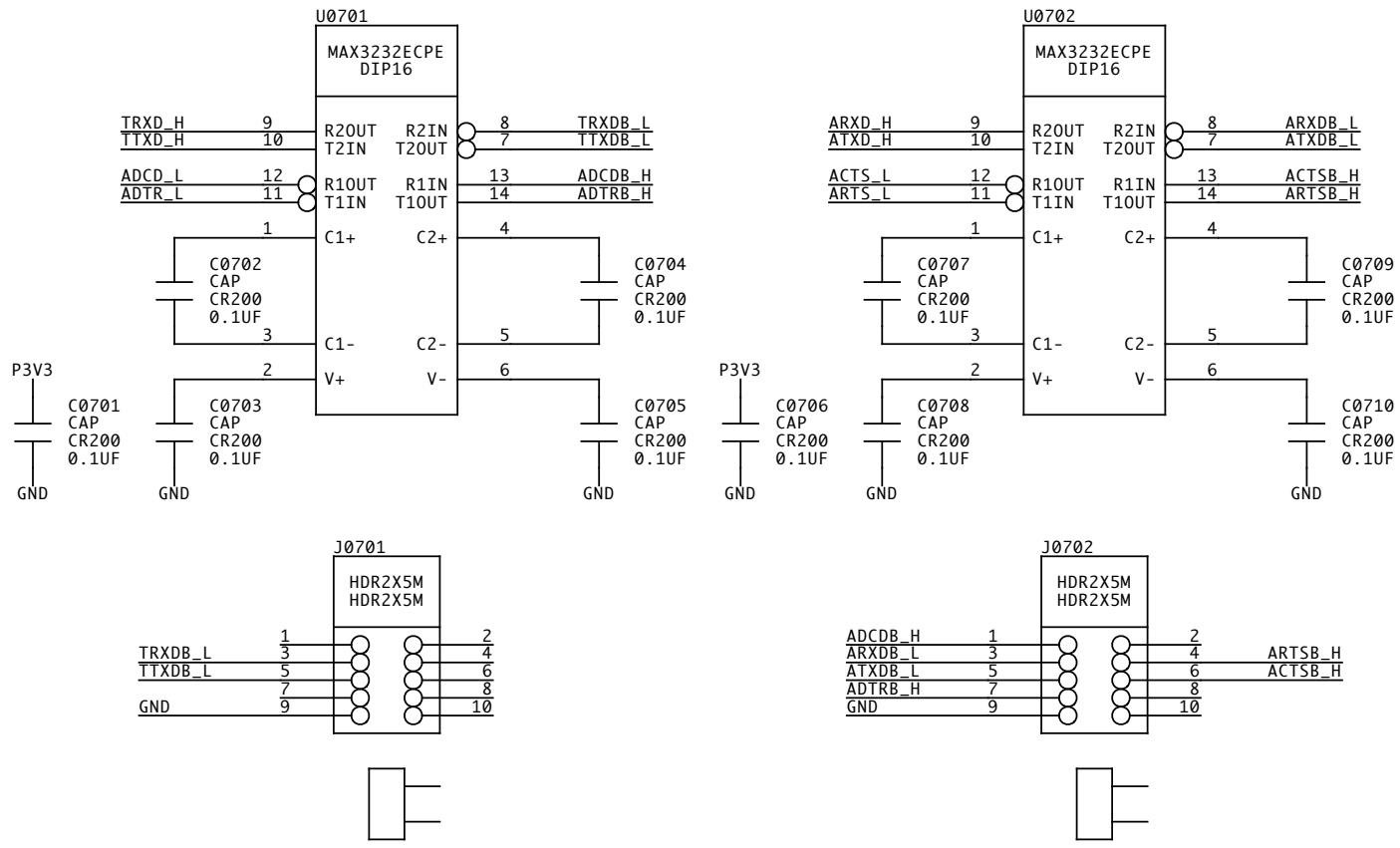






I2C USES 1.8K PULLU

PDP10X SYSTEM BOARD
 RTC DEVICE
 DGC, 30-OCT-2010

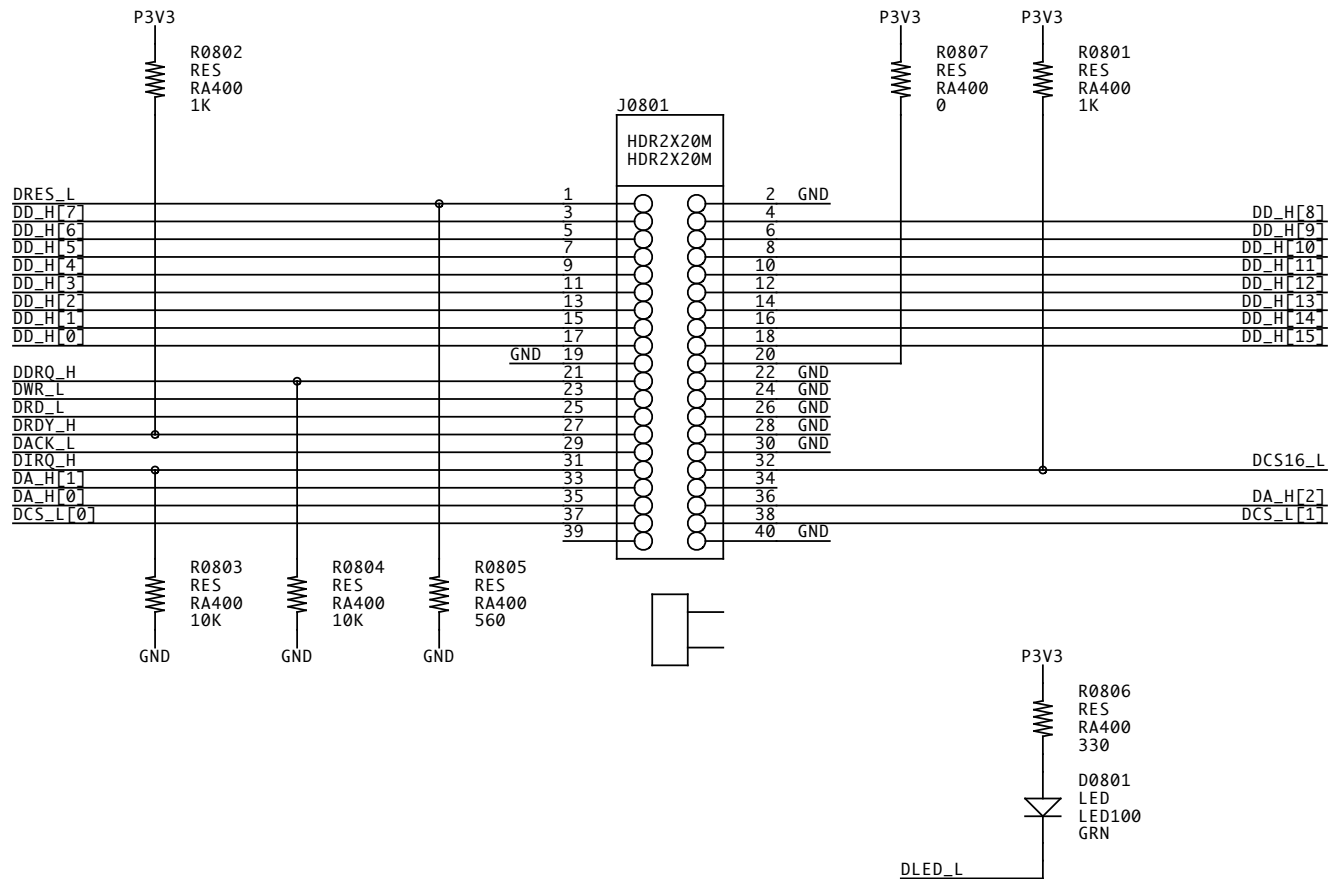


THE EXAR SP3232ECP-L IS A PIN-COMPATIBLE SUBSTITUTE FOR THE (SOMEWHAT MORE EXPENSIVE) MAX3232CEPE

THE TTY DEVICE PORT IS DATA-LEADS-ONLY

THE AUX DEVICE HAS RTS/CTS FOR FLOW CONTROL, DCD SO THE OPERATING SYSTEM CAN TELL IF A DIAL-UP MODEM DISCONNECTS, AND DTR TO ALLOW THE MODEM TO ACTUALLY CONNECT

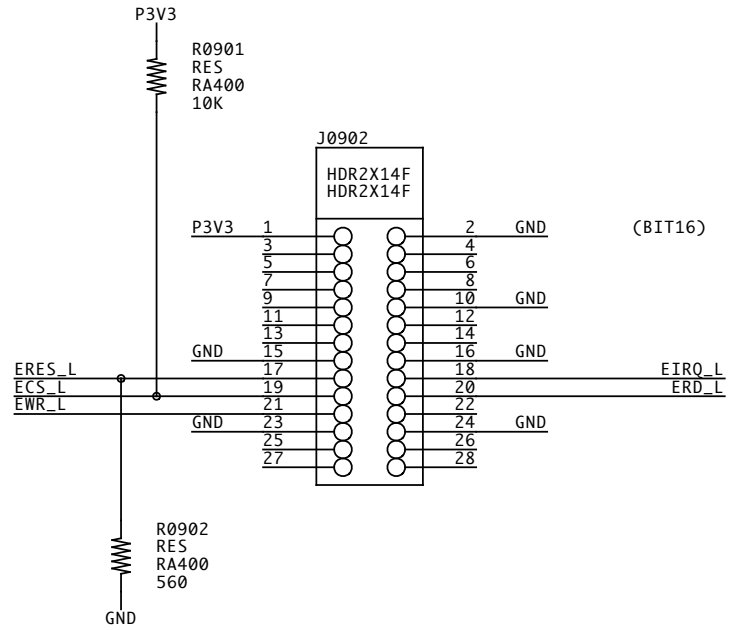
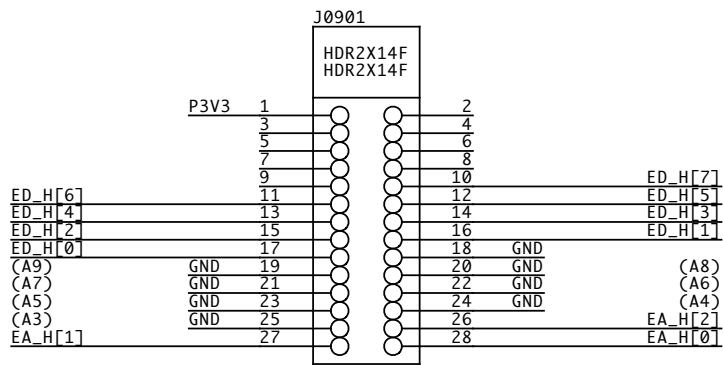
PDP10X SYSTEM BOARD
TTY DEVICE
DGC, 30-OCT-2010

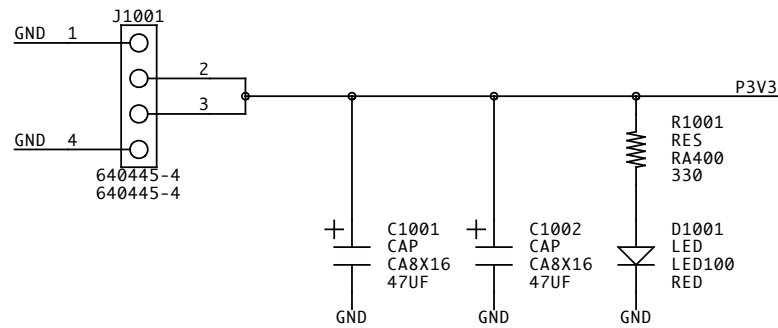


PIN 39 (DASP) IS NC (PULL-UP IN DISK)
 PIN 34 (PDIAG) IS NC (PULL-UP IN DISK)
 PIN 28 (CSEL) IS GND (MASTER)
 PIN 20 (KEY) IS VCC FOR EDC4000

THIS CONECTOR IS INTENDED TO BE USED WITH
 INNODISK EDC4000 40-PIN VERTICAL (DE0H-256D31CX5) MODULES

PDP10X SYSTEM BOARD
 DSK DEVICE
 DGC, 30-OCT-2010





XC3S500E	1.2	2.5	3.3	SYSTEM	3.3
9312 LUT/FF (100% UTIL), 33 MHZ, 50% SW, FAN=5	138			XC3S500E, LDO FROM 3.3, 400+100+300	800
20 BRAM, 100% EN, 33 MHZ, 50% SW, 2 PORTS	45			5 X AS6C4008-55	300
20 MULT, 33 MHZ, 50% SW	42			2 X MAX3232	2
9312 CLOCK LOADS, 33 MHZ	25			2 X 3.3V OSC (ABRACOM, 30-70 MHZ)	100
4 DCMS	4	64		ENC4000 (DSK, OPERATING)	140
126+32 8 MA LVTTTL SLOW, 33 MHZ, 50% SW, 50 PF	3	4	803	WIZ830MJ (OPERATING)	180
DYNAMIC TOTAL (MW)	257	68	803		
DYNAMIC TOTAL (MA)	214	27	243		
STATIC (MA)	106	31	1		
TOTAL (MA)	320	58	244	TOTAL (MA)	1540

PDP10X SYSTEM BOARD
 POWER CONNECTOR, BULK BYPASS
 DGC, 30-OCT-2010